



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hakewill, et al.
App. No. : 09/418,663
Filed : October 14, 1999
For : **METHOD AND APPARATUS
FOR MANAGING THE
CONFIGURATION AND
FUNCTIONALITY OF A
SEMICONDUCTOR DESIGN**
Examiner : Garcia-Otero, E.
Group Art Unit: 2123



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PATENT, TRADEMARK OFFICE

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, Washington, D.C. 20231, on

Thursday, October 16, 2003
(Date)

Robert F. Gazdzinski, Reg. No. 39,990

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Technology Center 2100

AMENDMENT AND RESPONSE TO OFFICE ACTION

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

Dear Sir:

In response to the Office Action dated July 16, 2003 ("Office Action"), please amend the above-identified application as follows:

IN THE SPECIFICATION

1. On Page 13, lines 11-20 of the specification as filed, please amend the text as follows:

--Referring now to Figure 2a, one specific embodiment of the general method of Figure 2 is described. In this embodiment, certain steps of the method depicted in Figure 2 are separated into constituent parts for illustration purposes. For example, step 102 of Figure 2 is separated into an interactive ("Wizard") ("wizard") component and a hierarchy generator ("hierarchy") component (not shown). The interactive component of step 102 provides substantially all of the direct user interaction control. Through a series of questions answered by the user, the program selects the relevant design elements necessary to realize the user's